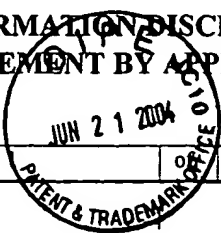


FORM PTO-1449/A and B (Modified)		APPLICATION NO.: 09/579,646	ATTY. DOCKET NO.: A0312.70363US00
INFORMATION DISCLOSURE STATEMENT BY APPLICANT		FILING DATE: May 26, 2000	CONFIRMATION NO.: 8011
		APPLICANT: Katsufumi Nakamura et al.	
		GROUP ART UNIT: 2615	EXAMINER: Brian C. Genco
Sheet			



U.S. PATENT DOCUMENTS

Examiner's Initials	Cite No.	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication or of issue of Cited Document MM-DD-YYYY
		Number	Kind Code		
BG	1	5,757,230		Mangelsdorf	05/26/1998
BG	2	6,441,684	B1	Nakamura	08/27/2002
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FOREIGN PATENT DOCUMENTS

Examiner's Initials	Cite No.	Foreign Patent Document			Name of Patentee or Applicant of Cited Document (not necessary)	Date of Publication of Cited Document MM-DD-YYYY	Translation (Y/N)
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OTHER ART — NON PATENT LITERATURE DOCUMENTS

Examiner's Initials	Cite No.	Include name of the author (in CAPITAL LETTERS) title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, relevant page(s), volume-issue number(s), publisher, city and/or country where published.	Translation (Y/N)
BG	1	Gregorian and Ternes, "Analog MOS Integrated Circuits for Signal Processing," Wiley-Interscience, 1986, pp. 412-417	

EXAMINER 	DATE CONSIDERED 5/9/05
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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